

UGC NET PAPER 3 NOVEMBER 05, 2017 SHIFT 1 ELECTRONIC SCIENCE QUESTION PAPER

This paper contains seventy five (75) objective type questions of two (2) marks each. All questions are compulsory.

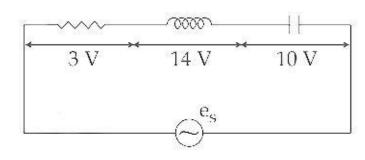
- In zone refining process if K is the distribution coefficient, C_S represents the concentration of dopant in solid phase and C_L represents the concentration of dopant in the liquid phase then K is defined as:

- (1) $K = C_S/C_L$ (2) $K = C_L/C_S$ (3) $K = \frac{C_S}{C_L} + 1$ (4) $K = \frac{C_L}{C_S} 1$
- Barrier height in a metal-semiconductor contact can easily be estimated from:
- (1) $\binom{1}{C}$ versus V curve (2) $\binom{1}{C^2}$ versus V curve (3) $\binom{1}{C}$ versus I curve (4) $\binom{1}{C^2}$ versus I curve

where C is capacitance, V is voltage and I is the current.

- Two coils having equal resistances but different inductances (Say L₁ and L₂) having time constants τ_1 and τ_2 respectively, are connected in series. The time constant, τ' of the series combination is:

 - (1) $\tau = \tau_1 + \tau_2$ (2) $\tau = \frac{\tau_1 + \tau_2}{2}$ (3) $\tau = \tau_1 / \tau_2$ (4) $\tau = \tau_1 \times \tau_2$
- The rms voltage measured across each of the circuit elements in the figure given below is shown. When the circuit is excited using a sinusoidal source 'es', the rms value of the source voltage 'e_s' is:



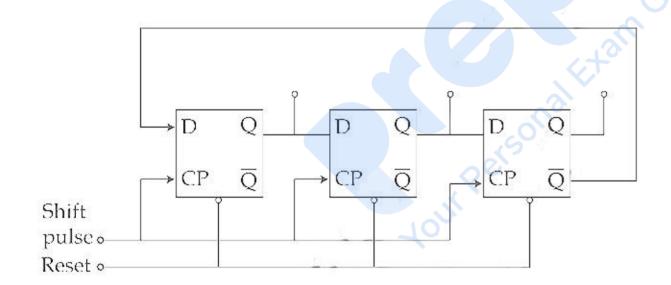
- (1)1 V
- (2)4 V
- 5 V (3)
- (4)10 V



- 5. For a given JFET, the typical values of amplification factor and transconductance are given as 40 and 100 μs respectively. The dynamic drain resistance of JFET will be:
 - (1) 200Ω

6.

- (2) 400Ω
- (3) $200 \text{ k}\Omega$
- (4) 400 k Ω
- For a given n-MOS capacitor with substrate doping of $N_A = 10^{17}$ cm⁻³, oxide thickness 20 nm, $\varepsilon_{\rm ox} = 3.9 \ \varepsilon_{\rm o}$ the values of oxide capacitance and flat band capacitance are respectively given as (use $\varepsilon_{\rm o} = 8.85 \times 10^{-14}$ F cm⁻¹):
 - (1) $142 \text{ nF/cm}^2 \text{ and } 172 \text{ nF/cm}^2$
- (2) $172 \text{ nF/cm}^2 \text{ and } 142 \text{ nF/cm}^2$
- (3) $71 \text{ nF/cm}^2 \text{ and } 86 \text{ nF/cm}^2$
- (4) 86 nF/cm² and 71 nF/cm²
- 7. A three bit shift register is shown below to have content 000 again, the number of clock pulses required would be:



(1) 2

(2)

(3) 6

- (4) 7
- 8. If $(11X1Y)_8 = (12C9)_{16}$, then the respective values of X and Y are:
 - (1) 1 and 5
- (2) 3 and 1
- (3) 5 and 7
- (4) 7 and 5
- 9. The flag which indicates whether the interrupt that may occur at the interrupt pins of 8086 will actually be served or not is:
 - (1) Direction flag

(2) Interrupt enable flag

(3) Trap flag

- (4) Overflow flag
- 10. The timer of 8155 consists of
- counter and operates in
- (1) 14 bit, 4 modes (2)
- (2) 16 bit, 8 modes (3)
- (3) 14 bit, 8 modes (4)
 - (4) 16 bit, 4 modes



11. Consider the following program of 'C'

```
main ()
{static int a[]-{1,2,3,4,5,6,7,8};
int i;
for(i=2;i<6;++i)
    a[a[i]]=a[i];
for(i=0;i<8;++i)
    printf ("%d",a[i]);
}</pre>
```

The output of the program is:

(1) 12345678

(2) 12335578

(3) 12233448

(4) None of the above

12. Assuming a 2-byte integer which of the following is the correct output for the program given below?

```
# include <stdio.h>
int main ()
{
printf("%x\n",-1<<3);
return0;
}</pre>
```

- (1) 0000
- (2) f f f f
- (3) fff8
- (4) 0008

13. A transmitting antenna of 100 m height radiates 40 kW at a frequency of 100 MHz uniformly in Azimuth plane. If the height of receiver antenna is 16 m, the value of Line of sight distance is given by:

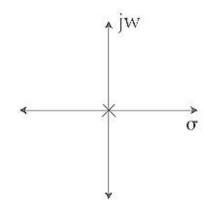
- (1) 25 km
- (2) 57.68 km
- (3) 27.68 km
- (4) 114 km



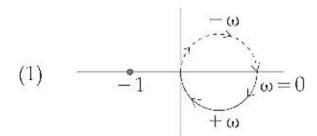
- The intrinsic impedance of a Lossy dielectric medium is given by: 14.
- $\frac{j\omega\mu}{\sigma}$ (2) $\sqrt{\frac{j\omega\mu}{\sigma+j\omega\epsilon}}$ (3) $\frac{j\omega\epsilon}{\mu}$ (4) $\sqrt{\frac{\mu}{\epsilon}}$
- The highest modulating signal frequency that can be demodulated by a peak detector without 15. attenuation is given by.
 - (1) $f_{\rm m}({\rm max}) = \frac{\sqrt{\left(\frac{1}{{\rm m}^2}\right) 1}}{2\,\pi\,{\rm RC}}$ (2) $f_{\rm m}({\rm max}) = \frac{\sqrt{{\rm m}^2 1}}{2\,\pi\,{\rm RC}}$ (3) $f_{\rm m}({\rm max}) = \frac{\sqrt{\left(\frac{1}{{\rm m}^2}\right) + 1}}{2\,\pi\,{\rm RC}}$
- In digital companding, the analog signal is:
 - First converted to a linear PCM Code and then sampled. (1)
 - First sampled and then converted to a linear PCM Code. (2)
 - (3)First sampled and then converted to a non - linear PCM Code.
 - Never sampled. (4)
- A Buck converter is: 17.
 - AC to DC converter (1)
- AC to AC converter
- (3)DC to DC converter
- DC to AC converter (4)
- A certain Helium Neon Laser emitting at 633 nm has a line width of 0.002 nm. The coherence 18. length of the laser is given by:
 - (1)40 cm
- (2)5 cm
- (3)20 cm
- 10 cm (4)

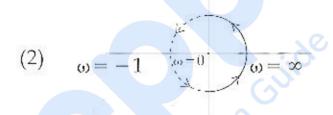


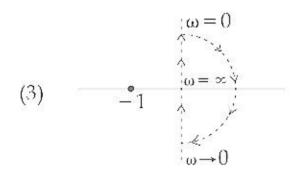
19. The root-locus of a typical transfer function is given as

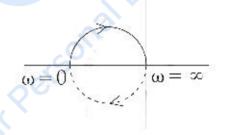


The polar plot of the transfer function will be:









(4)

20. The characteristics polynomial of a control system is given as

$$q(s) = s^4 + s^3 + s^2 + s + k$$

The system is:

- (1) Stable for all values of k
- (2) Stable for positive values of k
- (3) Unstable for all values of k
- (4) Unstable for only positive values of k

21. Conductivity of a semiconductor depends upon :

- (a) doping density
- (b) mobility of charge carriers
- (c) Length of semiconductor
- (d) Shape of semiconductor

Code:

- (1) (a), (b) and (c) are correct
- (2) (b), (c) and (d) are correct
- (3) (a) and (c) are correct
- (4) (a) and (b) are correct



22. A zener diode:

- (a) has sharp breakdown at low forward voltage
- (b) has sharp breakdown at low reverse voltage
- (c) can be used for voltage regulation
- (d) has a negative resistance region

Code:

23.

- (1) (a), (c) and (d) are correct
- (2) (b), (c) and (a) are correct
- (3) (a) and (b) are correct
- (4) (b) and (c) are correct

Consider the following statements associated with resonant frequency:

- (a) For a series RLC circuit, current is maximum
- (b) For a series RLC circuit, voltage across C is minimum
- (c) For a series RLC circuit, current is minimum
- (d) For a parallel RLC circuit, total impedance is maximum

Which one of the above statement are correct?

- (1) (a), (b) and (d) only
- (2) (b) and (d) only
- (3) (a) and (d) only
- (4) (a) and (b) only

24. Consider the following statements for the hybrid parameters:

- (a) h_{II} is called input impedance with output shorted
- (b) h₂₁ is current gain with output shorted
- (c) h₁₂ is voltage feedback ratio with input terminals shorted
- (d) h₂₂ is output admittance with input terminal open

Which of the statements given above are correct?

- (1) (a), (b) and (c) only
- (2) (a), (c) and (d) only
- (3) (a), (b) and (d) only
- (4) (a), (b), (c) and (d)



- 25. The capacitance of a reverse biased PN junction:
 - (a) increases as reverse bias is increased
 - (b) increases as reverse bias is decreased
 - (c) does not change by increasing or decreasing the reverse bias
 - (d) can be used for varactor diode application

Code:

26.

- (1) (a), (c) and (d) are correct
- (2) (b), (c) and (d) are correct
- (3) (b) and (d) are correct
- (4) (a) and (c) are correct
- For making an Oscillator certain conditions are to be met in an amplifier:
 - (a) -ve feedback
 - (b) + ve feedback
 - (c) Barkhausen criteria
 - (d) Noise signal

Code:

- (1) (b), (d), (c) is correct
- (2) (a), (d), (c) is correct
- (3) (a), (c) is correct
- (4) (b), (a), (c) is correct
- 27. Which of the following statements regarding binary counters are correct?
 - (a) Clock inputs of all the flip flops of a synchronous counter are applied from the same source whereas those in an asynchronous counters are from different sources.
 - (b) Asynchronous counter has ripple effects whereas synchronous counter has not.
 - (c) Only J-K flip-flops can be used in synchronous counters whereas asynchronous counters can be designed with any kind of flip flops.
 - (d) Latches can be used for designing asynchronous counters.

Code:

- (1) (a), (b) and (d) only
- (2) (a), (b) and (c) only
- (3) (b) and (d) only
- (4) (a), (c) and (d) only



28. Consider the following statement	S	
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A multiplexer

- (a) Selects one of the several inputs and transmits it to a single output
- (b) Routes the data from a single input to many outputs
- (c) Converts parallel data into serial data
- (d) Is a combinational circuit

Which of these statements are correct?

- (1) (a), (b) and (d) only
- (2) (b), (c) and (d) only
- (3) (a), (c) and (d) only
- (4) (b) and (c) only

29. Which of the following statements on DRAM are correct?

- (a) Page mode read operation is faster than RAS read.
- (b) RAS input remains active during column address strobe.
- (c) DRAM cell occupies more space than SRAM cell.
- (d) Static power is dissipated for storing charge in a capacitance.

The correct answer is:

- (1) (a) and (b)
- (2) (c) and (d)
- (3) (a) and (d)
- (4) (b) and (c)

30. Which of the following are control/handshake signal in mode 1 of 8255?

- (a) OBF
- (b) IBF

(2)

- (c) STB
- (d) BSR

Code:

- (1) (a) and (d)
 -)
- (a), (b) and (c)
- (3)
- (b), (c) and (d)
- (4)
- (a), (b) and (d)



Which of the following statements are correct about the program given below? 31. # include <stdio.h> int main() union a int x; Char C[2]; }; union a $K1 = \{1 \ 2 \ 3\};$ union a $K2=\{1,2,3\};$ return 0; } K2 cannot be initialized as shown (a) K1 can be initialized as shown (b) to initialize char C[] of K2 '.' operator should be used (c) results in syntax error (d)The correct answer is: (b) and (c) only (3) (a), (b) and (c) (1)(a) and (b) only (2) (4) (a), (c) and (d) Consider the following definition of array in C: 32. int a [] [2] [3]= $\{1, 2, 3\},\$ $\{4, 5, 6\}$ $\{10, 20, 30\},\$ {40, 50, 60} {100, 200, 300}, {400, 500, 600}

a[0] [0] [0] The correct answer is:

Which of the following expressions would give value 1 for the array defined above?

(c)

(1)(a) and (c)

};

(2)

(b)

(a) and (d)

* * # a

(a) and (b) (3)

** * a[0]

(c) and (d) (4)

(d)

* * a[0] [0]

33. A Series inverter with series Load deliver a load power of 1 kW at resonance. The value of Load resistance = 10Ω and resonant frequency is 20 kHz, Q factor = 3.85.

For this, the value of Capacitor and inductor are estimated. Following are the values of L and C:

(a) $L = 306.37 \mu H$

(b) $C = 0.2067 \mu F$

(c) $L = 3.06 \mu H$

(d) $C = 20.67 \mu F$

Out of above, which are correct?

(1) (a) and (b)

(2) (b) and (c)

(3) (c) and (d)

34.

(4) (a) and (d)

In SONET standards, following are the data rates and payloads:

- (a) SONET OC 1 data rate 51.84 Mbps
- (b) OC 1, payload 40.112 M bits/s
- (c) OC 1 data rate 50.112 Mbps
- (d) OC 1 payload 50.112 Mbps

Which of the above is correct for OC - 1?

- (1) (a) and (b)
- (2) (b) and (c)
- (3) (c) and (d)
- (4) (a) and (d)

35. Consider the following formulas with all the symbols having usual meanings:

(a)
$$P_t = P_c \left(1 + \frac{m}{2}\right)$$

(b)
$$I_t = I_c \left(1 + \frac{m^2}{2}\right)^{1/2}$$

(c)
$$P_t = P_c \left(1 + \frac{m^2}{2} \right)$$

(d)
$$I_t = I_c \left(1 + \frac{m}{2}\right)^{1/2}$$

Which of the above are incorrect?

- (1) (a) and (b)
- (2) (b) and (c)
- (3) (c) and (d)
- (4) (a) and (d)

36. Read the following statements:

- (a) A channel is said to be ideal, if, within the bandwidth occupied by the transmitted signal, the amplitude response is constant and phase response is a linear function of frequency.
- (b) A channel is said to be ideal, if, within the bandwidth occupied by the transmitted signal, both amplitude and phase response are linear function of frequency.
- (c) It is possible to compensate for the non-ideal frequency response characteristics of the channel by use of filter at the demodulator.
- (d) A non-ideal channel does not exist.

Which of the above statements are correct?

(1) (a) and (c) Only

(2) (a) and (d) Only

(3) (b) and (d) Only

(4) (b) and (c) Only



37. An X - band pulse cylindrical magnetron has following parameters:

Anode voltage = 26kV, Beam current = 27A, Magnetic flux density = 0.336 Wb/m² Radius of Cathode Cylinder = 5 cm, Radius of Vane edge to center = 10 cm.

Following data regarding angular frequency and Cut off voltage is given.

- (a) angular frequency = 5.91×10^{10} rad
- (b) Cut off voltage = 139.50 kV
- (c) angular frequency = 3.91×10^9 radian
- (d) Cut off voltage = 239.39 kV

Which is a correct option?

- (1) (a) and (b) (2) (b) and (c) (3) (c) and (d) (4) (d) and (a)
- 38. A TE₁₁ mode is propagating through a circular waveguide of radius of 5 cm. The dielectric in the waveguide is air. For this following are the datas.
 - (a) Cut off wave number = 38.62 (b) Cut off frequency = 1.758 GHz
 - (c) Cut off wave number = 36.82 (d) Cut off frequency = 2.758 GHz

Out of these following are correct:

- (1) (a) and (b) (2) (b) and (c) (3) (c) and (d) (4) (a) and (d)
- 39. Read the following statements:
 - (a) A function G(s) of the complex variables 'S' is called an analytic function in a region of the S-plane if the function and all its derivatives exist in the region.
 - (b) A function G(s) of the complex variable 'S' is called an analytic function in a region of the S-plane if function and all its derivatives do not exist in the region.
 - (c) The singularities of a function are the points in the S-plane at which the function and its derivatives do not exist.
 - (d) The singularities of a function are the points in the S-plane at which the function and its derivatives exist.

Which of the above statements are **not correct**?

- (1) (a) and (b)
- (2) (b) and (c)
- (3) (b) and (d)
- (4) (a) and (d)

- A force with which the plates of a parallel plate capacitor having charge Q and area of each 40. plate A, attract each other is:
 - Directly proportional to Q (a)
- (b) Inversely proportional to Q
- Directly proportional to Q² (c)
- Inversely proportional to A (d)

Which of the above are correct?

(1)(a) and (d) Only

(b) and (d) Only (2)

(3)(c) and (d) Only

(a), (c) and (d) Only (4)

41. Match the following list:

List - I

List - II

Maximum efficiency of a Class B amplifier (a)

100% (i) (ii) 78.5%

Maximum efficiency of a Class A amplifier (b)

Maximum efficiency of a Class C amplifier (c)

- (iii) 50%
- Rectification efficiency of half wave rectifier when rd << RL (d)
- 40.6% (iv)

Correct code is:

- (a) (b)
- (c) (d)
- (1)(i) (ii)
- (iii) (iv)
- (2)(ii)

- (iii) (i)
- (iv)
- (3)
- (ii)(iv) (i)
- (iii) (i)
- (4)(iv)
- (iii) (ii)
- 42. Match the following list:

List - I

List - II

(i)
$$\frac{V_2}{I_2}$$
 at $I_2 = 0$

(ii)
$$\frac{V_1}{I_1}\Big|_{X_1 \times Y_2 = 0}$$

(iii)
$$\frac{V_I}{I_1}\Big|_{at I_2 = 0}$$

(iv)
$$\frac{I_2}{V_2}\Big|_{at I_1 = 0}$$

Correct code is:

- (a)
- (b)
- (c) (d)

(ii)

(ii)

(i)

- (1)(iii)
- (iv)
- (i)
- (2)

(3)

(4)

(i)

(ii)

- (iii)
- (iv) (iii)
- (iv)

- (iii) (ii)
- - (iv)
 - (i)

Match the following list: 43.

List - I

- Boltzman Approximation gives (a)
- Total number of holes in valence band (b)
- Position of Fermi level (c)
- Debye screening length (d)

Correct code is:

- (a) (b) (c) (d)
- (1)(iii) (ii) (i) (iv)
- (2)(iv)(ii)(iii) (i)
- (3)(iv) (i) (ii) (iii)
- (4)(iii) (ii) (i) (iv)
- Match the following list: 44.

List - I

(Boolean logic function)

- ab + bc + ca(a)
- $ab + \overline{a} \overline{b} + \overline{c}$ (b)
- (c) a + bc
- $(\overline{a}+\overline{b}+\overline{c})(a+\overline{b}+\overline{c})(\overline{a}+\overline{b}+c)$

Correct code is:

- (a) (b) (c) (d)
- (1)(ii) (i) (iv) (iii)
- (2)(i) (iv) (ii)(iii)
- (3)(iii) (ii) (i) (iv)
- (4)(ii)(iii) (iv)(i)

List - II

(i)
$$\left[\frac{KT}{q^2n} \, \epsilon_s \, \epsilon_0\right]^{\frac{1}{2}}$$

- $\int_{-\infty}^{E_{v}} g_{\sigma}(E) f_{h}(E) dE$
- (iv) $f_e(E) = \exp[-(E E_f)/kT]$

List - II

(Inverse of function)

- $\bar{a} (\bar{b} + \bar{c})$ (i)
- $\overline{a} \overline{b} + \overline{b} \overline{c} + \overline{c} \overline{a}$ (ii)
- (a ⊕ b) c (iii)
- $abc + \overline{a}bc + ab\overline{c}$ (iv)



Match the following list: 45.

List - I

List - II

- **PCON** (a)
- (i) contains status information
- TMOD (b)
- (ii) timer/counter control register
- **TCON** (c)
- idle bit, power down bit (iii)
- (d)**PSW**
- timer/counter modes of operation (iv)

Correct code is:

(a) (b) (c) (d)

- (1)(i) (iii) (iv) (ii)
- (2)(iv) (ii) (i) (iii)
- (3)(iii) (iv) (i) (ii)
- (4)(iv) (ii) (i) (iii)

Match the following list: 46.

List - I

(Storage class)

(Storage, initial - value, scope)

List - II

auto (a)

- register, garbage, local (i)
- register (b)
- (ii) memory, zero, global

(c) static

(iii) memory, garbage, local

(d) extern

(iv) memory, zero, local

- (a) (b)
- (c)
- (d)

(i)

(iii)

- (1) (iv)
- (ii)
- (iii)
- (iv)
- (ii)
- (2)

(3)

(ii)

(i)

- (iii)
- (i)
- (iv)
- (iii)
- (4)(i) (iv) (ii)



Match the following lists in terms of Microwave bands: 47.

List - I

List - II

(Bands)

(Frequency)

- UHF (a)
- 300 3000 kHz (i)
- MF (b)
- 26 40 GHz (ii)
- (c) K_a
- 4 8 GHz (iii)
- (d)C
- 300 3000 MHz (iv)

Correct code is:

- (a)
- (b) (c)
- (d)

- (1)
- (i) (ii)
- (iv)
 - (iii)

- (2)
- (iv)
- (i)

(ii)

(iii)

(3)

(4)

- (i)

(i)

(ii)

- (ii) (iv)
- (iii)

(iv)

- (iii)
- Match the following list: 48.

List - I

- Super heterodyne Receiver (a)
- (b) PCM
- Delta modulation (c)
- (d) FM

(a)

(iii)

- (b)
- (c)
- (1)
- (i)
- (ii) (iv)
- (2)

(4)

- (iii)
- (iv) (i)
- (iv)

(i)

- (3)(ii)

(ii)

- (iv)
- (i)
- (ii) (iii)

(d)

(iii)

- List II
- Granular Noise (i)
- Image frequency (ii)
- Frequency discrimination (iii)
- Quantization Noise (iv)

Match the following list: 49.

List - I

List - II

- Mach Zehnder (a)
- Direct modulation (i)
- On Off Keying (b)
- Stimulated emission (ii)

SRS (c)

- Pulse broadening (iii)
- Dispersion (d)
- Indirect modulator (iv)

Correct code is:

- (a)
- (b)
- (c) (d)
- (1)
- (iv)
- (ii)
- (iii) (i)
- (2)
- (iv)
- (i)
- (ii) (iii)
- (3)
- (i)
- (ii)
- (iii) (iv)
- (4)(ii)
- (i)
- (iii) (iv)

Match the following list: 50.

List - I

- Tachogenerator (a)
- Electrical to mechanical signal (i)

- LVDT (b)
- Rotating transformer (ii)
- Synchro (c)
- Displacement into voltage (iii)

List - II

- Servo motors (d)
- Rotary motion into electrical signal (iv)

Correct code is:

- (a)
- (b)
- (d) (c)
- (1)
 - (iii)
- (iv)
- (i) (ii)
- (2)
 - (iv)
- (iii)
 - (ii) (i)
- (3)(4)
- (iii)

(iv)

(iv)

(iii)

- (i) (ii) (ii) (i)
- A given semiconductor is doped by phosphorous at different doping concentrations (given 51.

(d)

 $2 \times 10^{12} \text{ cm}^{-3}$ (a)

 4×10^{14} cm 3 (b)

 3×10^{13} cm $^{-3}$ (c)

 2×10^{15} cm 3

- Sequence them in terms of decreasing conductivity:
- (a), (b), (c), (d) (1)

(d), (b), (c), (a) (2)

(b), (a), (d), (c) (3)

(d), (c), (a), (b) (4)

- **52.** What are the steps required in sequence to solve a circuit using super position theorem?
 - (a) Select any one source and short circuit all other voltage sources.
 - (b) Find out the current or voltage across the required element, due to the source under consideration.
 - (c) Open all current sources, if internal impedance is not known. If known replace them by their impedances.
 - (d) Add all the individual effects produced by individual sources to obtain the total current in or across the voltage element.

Code:

53.

(1) (a), (c), (b), (d)

(2) (a), (b), (c), (d)

(3) (c), (a), (b), (d)

- (4) (a), (d), (c), (b)
- Following are the amplifying systems available.
- (a) CB transistor amplifier
- (b) CC transistor amplifier
- (c) CE transistor amplifier
- (d) OP Amp

Sequence in terms of increasing order of voltage gain.

(1) (b), (a), (c), (d)

(2) (d), (c), (a), (b)

(3) (a), (b), (c), (d)

- (4) (c), (d), (b), (a)
- 54. The correct sequence of shifting binary number through bidirectional shift register is:
 - (a) All the lower registers are reset i.e. set = 0 by giving a pulse at the reset terminals.
 - (b) The contents of the lower register are gated up directly to the upper register.
 - (c) The upper register is reset for the next shift operation.
 - (d) The contents of the upper register are gate down to the lower register either one position to the right or to the left as desired.

Code:

(1) (a), (c), (b), (d)

(2) (b), (a), (c), (d)

(3) (b), (a), (d), (c)

(4) (a), (c), (d), (b)

55.	Arrange the	following	operations	of DMA	to transfer	a block of data.
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- (a) Terminal count register is decremented by one with each byte of data transfer till it reaches to zero.
- (b) DMA controller acquires bus system and starts transferring the data.
- (c) Microprocessor suspends its operations, release the buses and generates HLDA.
- (d) DMA controller sends HOLD

The correct sequence is:

(1) (d), (b), (c), (a)

(2) (d), (c), (b), (a)

(3) (d), (c), (a), (b)

(4) (c), (d), (b), (a)

56. Arrange the following operators in the precedence order of highest to lowest:

- (a) = =
- (b) + +
- (c) %
- (d) &

The correct order of precedence is:

(1) (d), (b), (c), (a)

(2) (b), (c), (a), (d)

(3) (a), (d), (b), (c)

(4) (c), (a), (d), (b)

57. Following are various layers in atmosphere used for communications:

- (a) F₂ Layer
- (b) D layer
- (c) F₁ Layer
- (d) E Layer

Arrange these in terms of descending order of their height from earth.

The correct sequence is:

- (1) (a), (c), (d), (b)
- (2) (b), (c), (d), (a)
- (3) (a), (b), (d), (c)
- (4) (c), (b), (a), (d)

58. Consider the following detector circuits.

(a) Diode detector

- (b) Slope detector
- (c) Balanced slope detector
- (d) Ratio detector

Arrange the above detectors in decreasing order of their circuit complexity:

- (1) (d), (b), (c), (a)
- (2) (a), (b), (c), (d)
- (3) (c), (d), (b), (a)
- (4) (d), (c), (b), (a)



59. Following are the wavelength bands used in optical	communication systems.
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(a) Long (L) Band

- (b) Conventional band (C band)
- (c) Extended band (E band)
- (d) Original band (O-band)

Arrange them in increasing order of wavelengths. The correct sequence is :

- (1) (d), (c), (b), (a)
- (2) (c), (b), (a), (d)
- (3) (b), (c), (a), (d)
- (4) (a), (b), (c), (d)

60. Consider the following piezoelectric materials.

- (a) Quartz
- (b) Tourmaline
- (c) Lithium sulphate

Arrange the above piezoelectric materials in decreasing order of their useful temperature ranges :

- (1) (a), (b), (c)
- (2) (a), (c), (b)
- (3) (b), (a), (c)
- (4) (b), (c), (a)

Directions: Question No. 61 to 70:

The following items consist of two statements, one labelled as "Assertion (A)" and the other labelled as the "Reason (R)". You are to examine the two statements carefully and decide if the Assertion (A) and the Reason (R) are individually true and if so whether the reason is a correct explanation of the assertion. Select your answer to these items using the codes given below and mark your answer accordingly.

Code:

- (1) Both (A) and (R) are true and (R) is the correct explanation of (A).
- (2) Both (A) and (R) are true, but (R) is not the correct explanation of (A).
- (3) (A) is true, but (R) is false.
- (4) (A) is false, but (\mathbb{R}) is true.



61. Assertion (A):

A p-n junction diode with low junction depth can be used as solar cell when illuminated by suitable light (wavelength) at top thin layer above the junction.

Reason (R):

A p-n junction diode under no condition can be used as solar cell. It can be used only as light emitting diode for optoelectronic applications.

62. Assertion (A):

Superposition theorem can be used to find the output of a full wave rectifier excited by sinusoidal signal sources of different frequencies connected in series.

Reason (R):

Superposition theorem is valid for all linear systems.

63. Assertion (A):

A UJT (Unijunction Transistor) finds application in making relaxation oscillator of certain given frequency.

Reason (R):

It has three terminals like that of a transistor - corresponding to emitter base and collector. Therefore it can be used to make oscillator as well as amplifier.

64. Assertion (A) :

It is possible that a digital circuit gives the same output for different input voltages.

Reason (R):

A digital circuit is also called logic circuit.

65. Assertion (A):

The address bus size in 8086 is 20 bit.

Reason (R):

Registers size of 8086 microprocessor is 16 bit.

66. Assertion (A):

C++ is object - oriented programming language.

Reason (R):

C++ supports class, inheritance, templates and exception handling.



Assertion (A): 67.

In power electronics, converters feel stress during the operation beyond their ratings.

Reason (R):

The snubber circuits are used to increase the applied voltage across the devices during turn off transients.

68. Assertion (A):

Sampling theorem forms the basis for the relation between continuous time signals and discrete time signals.

Reason (R):

If the sampling is done at Nyquist rate, the signal x(t) can be reconstructed from the sample values $x[n] = x[nT_s]; -\infty \le n \le \infty$.

69. Assertion (A):

A reflex Klystron is a single cavity Klystron that overcomes the disadvantages of two cavity Klystron oscillator.

Reason (R):

It is high power microwave generator operating beyond 60 GHz and generating powers in kWs. Its efficiency is very high of the order of 70%.

70. Assertion (A):

Capacitive transducers are, infact, primary transducers for measurement of displacements.

Reason (R):

A Capacitive transducer is a non loading, non contact and non-invasive type transducer for displacement measurements.

Based on the paragraph given below answer Q. 71 to 75:

The traditional methods of combinational circuits involve simplification and realization using gates. Using these methods, complex functions have been integrated and are easily available in IC form. There is an attractive array of devices like multiplexer, demultiplexer, decoders, comparators, parity generators/checkers, which significantly reduce IC package count there by reducing the system cost. The system design is greatly simplified because the laborious and time consuming simplification methods are generally not required with these devices. This also improves the reliability of the system by reducing the number of external wired connection. But they have some limitations as well. Different memories like ROM, PROM can also be used to implement combinational circuits without much simplification.

- 71. The minimum number of selection lines required for selecting one of the 16 inputs are:
 - (1)

(2)

2

- (3)8
- (4)16
- A logic circuit required for converting BCD Code to 7 segment Code is known as : 72.
 - Multiplexer
- Demultiplexer (3) Decoder (2)
- (4)Encoder

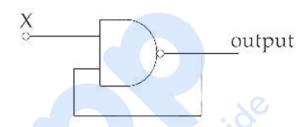
- 73. In a BCD Subtractor if the sum of minuend and the nine's complement of subtrahend is not a valid BCD Code, then which binary number is added to the sum to get the correct answer?
 - (1) 1000
- (2) 0001
- (3) 0110
- (4) 0111

- 74. Consider the following digital circuits:
 - (a) Multiplexers

(b) Read only memories

(c) D - Latch

(d) Circuit as shown



Which of these come under the class of combinational circuits only?

- (1) (a) and (b)
- (2) (c) and (d)
- (3) (b) and (c)
- (4) (b), (c) and (d)
- 75. A ROM is to be used to implement a "Squarer", which outputs the square of a 3-bit number. What must be the size of the ROM?
 - (1) 8 address lines and 9 data lines
 - (2) 3 address lines and 6 data lines
 - (3) 3 address lines and 9 data lines
 - (4) 8 address lines and 6 data lines



Space For Rough Work

